Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1103	low near pressure near lamp	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:07
L2	439	invert\$3 and switch\$3 and transistor and half near bridge and (phase) and alternat\$3 near current	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25
L3	4 ,	l1 and l2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:11
L4 .	6	invert\$3 and transistor and half near bridge and (phase near sett\$3) and alternat\$3 near current	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:24
L5	2508	315/291.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:24
L6	1675	315/224.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25
L7	1985	315/307.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:24
L8	1429	315/dig.5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25
L9	1247	315/dig.7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25
L10	520	315/225.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25

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L11	199	invert\$3 and switch\$3 and transistor and half near bridge and (phase) and alternat\$3 near current and lamp	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25
L12	161	l11 and @ad<="20020108"	US-PGPUB; USPAT;	OR	OFF	2004/12/12 15:26
			USOCR; EPO; JPO; DERWENT			
L13	47	I12 and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:26
L14	55	I12 and 16	US-PGPUB; USPAT;	OR	OFF	2004/12/12 15:26
			USOCR;		4	
			EPO; JPO; DERWENT			
L15	65	I12 and I7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:26
L16	26	l12 and l8	US-PGPUB; USPAT;	OR	OFF	2004/12/12 15:26
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			EPO; JPO;			
		en e	DERWENT	Maring Desired	ļ	医生物乳肉 医囊膜丛
L17	52	112 and 19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:26
L18	9	l12 and l10	US-PGPUB;	OR	OFF	2004/12/12 15:26
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ž.	, **		EPO; JPO; DERWENT		عاد الماد الم	

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1103	low near pressure near lamp	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 18:55
L2	439	invert\$3 and switch\$3 and transistor and half near bridge and (phase) and alternat\$3 near current	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 18:12
L3	4	11 and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ÖFF	2004/12/12 15:11
L4	6	invert\$3 and transistor and half near bridge and (phase near sett\$3) and alternat\$3 near current	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:24
L5	2508	315/291.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:24
L6	1675	315/224.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25
L7	1985	315/307.ccls.	US-PGPUB; USPAT;	OR	OFF	2004/12/12 15:24
			USOCR; EPO; JPO; DERWENT			
L8	1429	315/dig.5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25
L9	1247	315/dig.7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25
L10	520	315/225.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25

L11	199	invert\$3 and switch\$3 and transistor and half near bridge and (phase) and alternat\$3 near current and lamp	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:25
L12	161	l11 and @ad<="20020108"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 18:10
L13	47	i12 and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:26
L14	55	I12 and I6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:26
L15	65	l12 and l7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:26
L16	26	I12 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:26
L17	52	l12 and l9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:26
L18	9.	l12 and l10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 15:26
L19	156	invert\$3 and switch\$3 and transistor and half near bridge and (phase) and alternat\$3 near current and mosfet	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 18:09
L20	86	invert\$3 and switch\$3 and transistor and half near bridge and (phase) same inverter\$3 and alternat\$3 near current and mosfet	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 18:10

124		120 0 4 20020100	LIC DCDUB.	OR	OFF	2004/12/12 18:11
L21	57		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT		OFF	2004/12/12 16.11
L22	0	I21 and I1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 18:11
L23	4	invert\$3 and transistor and half near bridge and (phase) and alternat\$3 near current and l1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 18:36
L24	2	invert\$3 and transistor and half near bridge and (phase) and alternat\$3 near current and l1 and mosfet	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 18:37
L25	2	low near pressure near lamp and mosfet and transistor and I2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 18:58
L26	7	low near pressure near lamp and mosfet and transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2004/12/12 18:59